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0001/PTO Rev. 10/95	U.S. Department of Commerce Patent and Trademark Office	Application Number	09/629,780
TRANSMITTAL FORM (to be used for all correspondence during pendency of filed application)		Filing Date	July 31, 2000
		First Named Inventor	Yowjuang W. Llu
		Group Art Unit Number	2814
		Examiner Name	Not Yet Known
Total Number of Pages in This Submission	10	Attorney Docket Number	5251

#3/CFR
11/28/00
✓8

ENCLOSURES (check all that apply)	
<input type="checkbox"/> Fee Transmittal Form (in duplicate) <input type="checkbox"/> Check Enclosed	<input type="checkbox"/> Issue Fee Transmittal
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Signature:		
Attorney/Reg. No.:	Trinidad Arriola Kern/Reg. No. 44,012	Dated: 10/12/2000

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I hereby certify that this correspondence, including the enclosures identified above, is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Washington, D.C. 20231 on the date shown below. If the Express Mail Mailing Number is filled in below, then this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service pursuant to 37 CFR 1.10.		
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IN THE UNITED STATES

PATENT AND TRADEMARK OFFICE

APPLICANT(S): Yowjuang W. Liu and Donald L. Wollesen
APPLICATION NO.: 09/629,780
FILING DATE: July 31, 2000
TITLE: A Trenched Gate Non-Volatile Semiconductor Method With the
Source/Drain Regions Spaced From the Trench by Sidewall
Dopings
EXAMINER: Not Yet Known
GROUP ART UNIT: 2814
ATTY. DKT. NO.: 5251

CERTIFICATE OF MAILING

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Trinidad Arriola Kern, Reg. No.: 44,012

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APPLICATION PROCESSING DIVISION
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WASHINGTON, DC. 20231

REQUEST FOR CORRECTED FILING RECEIPT

SIR:

Enclosed is a copy of the Official Filing Receipt. It contains the following errors:

1. The Inventors are erroneously stated as only Donald L. Wollesen. The Filing Receipt should be corrected to indicate the names of the Applicants as Yowjuang W. Liu and Donald L. Wollesen. That there should be two (2) inventors listed under Applicant(s) is evidenced by the executed Declaration, a copy of which is enclosed.

2. The title is erroneously stated as "Trenched Gate Non-Volatile Semiconductor Device and Method." The correct title is "A Trenched Gate Non-Volatile

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PATENT


Semiconductor Method With the Source/Drain Regions Spaced From the Trench by Sidewall

Dopings", as evidenced by the Preliminary Amendment, a copy of which is enclosed.

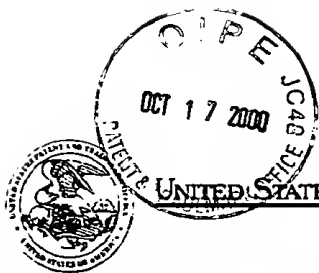
Please issue a corrected Filing Receipt rectifying these errors.

Respectfully submitted,
YOWJUANG W. LIU AND
DONALD L. WOLLESEN

Dated: 10/12/00

By: 
Trinidad Arriola Kern, Reg. No.: 44,012
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/629,780 ✓	07/31/2000 ✓	2814	690 ✓	5251	8 ✓	5 ✓	1 ✓

 Albert C Smith
 Fenwick & West LLP
 Two Palo Alto Square ✓
 Palo Alto, CA 94306
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OCT 02 2000

FENWICK & WEST LLP

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OC000000005422878

Date Mailed: 09/25/2000

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

Applicant(s)

 Youjuang W. Liu and
 Donald L. Wollesen, Saratoga, CA ;

Continuing Data as Claimed by Applicant

Foreign Applications

If Required, Foreign Filing License Granted 09/22/2000

Title

Trenched gate non-volatile semiconductor device and method

 the Source/Drain Regions
 with corner doping spaced
 and sidewall doping from
 the Trench by
 Sidewall Doping

Preliminary Class

257

Data entry by : SYDNOR, RUTH

Team : OIPE

Date: 09/25/2000


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Bib Data Sheet

SERIAL NUMBER 09/629,780	FILING DATE 07/31/2000 RULE -	CLASS 257	GROUP ART UNIT 2814	ATTORNEY DOCKET NO. 5251
APPLICANTS Yowjuang W. Liu, San Jose, CA ; Donald L. Wollesen, Saratoga, CA ; ** CONTINUING DATA ***** ** FOREIGN APPLICATIONS *****				
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 09/22/2000				
Foreign Priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged _____ Examiner's Signature Initials		STATE OR COUNTRY CA	SHEETS DRAWING 8	TOTAL CLAIMS 5
				INDEPENDENT CLAIMS 1
ADDRESS Albert C Smith Fenwick & West LLP Two Palo Alto Square Palo Alto, CA 94306				
TITLE Trenched gate non-volatile semiconductor method with the source/drain regions spaced from the trench by sidewall dopings				
FILING FEE RECEIVED 690	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:		<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit	